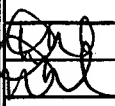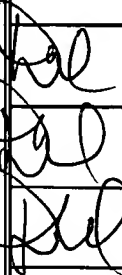| Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 207202US2		SERIAL NO. NEW APPLICATION
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Motoaki TANIZAWA		
		FILING DATE Herewith		GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
	AO	5-233592	9/10/93	Japan (with Partial English Translation)		X
	AP	6-119308	4/28/94	Japan (with Partial English Translation)		X
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Man-Kuan VAI, et al., "Modeling of Microwave Semiconductor Devices Using Simulated Annealing Optimization", IEEE Transactions on Electron Devices, Vol. 36, No. 4, pp. 761-762, April 1989
	AX	Takayasu SAKURAI, et al., "Fast Simulated Diffusion: An Optimization Algorithm for Multiminimum Problems and Its Application to MOSFET Model Parameter Extraction", IEEE Transactions on Computer-Aided Design, Vol. 11, No. 2, pp. 228-233, February 1992
	AY	Rob A. RUTENBAR, "Simulated Annealing Algorithms: An Overview", IEEE Circuits and Devices Magazine, pp. 19-25, January 1989
	AZ	

Examiner	Date Considered 12-2-04

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.